FPGA BASED MAXIMUM POWER POINT TRACKER OF PARTIALLY SHADED SOLAR PHOTOVOLTAIC ARRAYS USING MODIFIED ADAPTIVE PERCEPTIVE PARTICLE SWARM OPTIMIZATION

Shubhajit Roy Chowdhury\textsuperscript{1}, Dipankar Mukherjee\textsuperscript{2}, Hiranmay Saha\textsuperscript{2*},
\textsuperscript{1,3}IC Design and Fabrication Centre,
Department of Electronics and Telecommunication Engineering,
Jadavpur University, India
\textsuperscript{2}Department of Electronics and Telecommunication Engineering,
Bengal Engineering and Science University, Shibpur, India
Emails: \{\textsuperscript{1}shubhajit, \textsuperscript{3}hsaha\}@juiccentre.res.in
\textsuperscript{2}dipankarm@rediffmail.com

ABSTRACT
The paper presents a Field Programmable Gate Array (FPGA) based tracker to accurately track the maximum power point (MPP) of a photovoltaic (PV) array. The tracking logic realized on FPGA is based on a modified version of Adaptive Perceptive Particle Swarm Optimization (APPSO) technique. Photovoltaic generation systems use MPP tracker because the photovoltaic array exhibits multiple maxima in the power voltage characteristic under partially shaded conditions. Compared to PSO, the APPSO offers flexibility in the motion dynamics of the particle in the search space through variation in perception radius, number of sampling points per directions, and the number of sampling directions. The APPSO algorithm has been suitably modified to suit to the slight changes in the maximum power point at around the maximum power point. The proposed technique uses only one pair of sensors to control multiple PV arrays. This results in lower cost, higher accuracy and also the algorithm is simple. The implementation of the algorithm on a reconfigurable architecture like FPGA ensures hardware based flexibility in the motion dynamics presented by APPSO. A comparative study is performed to compare the performance of PSO and APPSO with respect to MPP tracking. Compared to PSO that track to the MPP under partial shading conditions and reaches the MPP with 96.41% accuracy, the APPSO can track to the MPP with 97.95% accuracy. The algorithm when realized on an Altera Cyclone EP1C6Q240C8 FPGA consumes 5967 logic blocks.